

WEST Search History

DATE: Tuesday, December 05, 2006

Hide?	<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>
		<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L12	L11 and l2	4
<input type="checkbox"/>	L11	sony.as. and ((parallel or simultaneous\$) near5 process\$) and (stream near5 video)	193
<input type="checkbox"/>	L10	L9 and ((parallel or simultaneous\$) near5 process\$)	3
<input type="checkbox"/>	L9	(Dorojevets or Ogura).in. and (vector near5 register)	7
<input type="checkbox"/>	L8	((process\$ near5 slice) same (alu or arithmetic logic unit) and ((read\$ or register\$ or stor\$) near5 vector) and (stream near5 (video or picture)) same \$4block)	2
<input type="checkbox"/>	L7	vector register and block register and scalar register and arithmetic unit and ((simultaneous\$ or parallel or pipeline) near7 process\$)	1
<input type="checkbox"/>	L6	L3 and l4	1
<input type="checkbox"/>	L5	L4 and l2	4
<input type="checkbox"/>	L4	((vector register same (block or macroblock)) and ((alu or arithmetic logic unit) same (process\$ or co\$process\$ or pipeline or (identical or parallel or simultaneous\$)) adj4 process\$))	13
<input type="checkbox"/>	L3	L2 and (((stream or packet) near9 video) same slice)	31
<input type="checkbox"/>	L2	(((parallel or simultaneous\$ or identical\$) near7 process\$) same vector) and (video near5 process\$)	640
<input type="checkbox"/>	L1	(345/\$.ccls. or 348/\$.ccls.) and ((processing element or co\$processor or pipeline or (identical or parallel) processor) same vector register same scalar register)	1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
(two-dimension\$ array same processing element same arithmetic logic unit).clm.	1

Database:

US Pre-Grant Publication Full-Text Database

US Patents Full-Text Database

US OCR Full-Text Database

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Search:

L3

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<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u> result set
<i>DB=PGPB; PLUR=YES; OP=ADJ</i>			
<u>L3</u>	(two-dimension\$ array same processing element same arithmetic logic unit).clm.	1	<u>L3</u>
<u>L2</u>	(two-dimension\$ array same processing element same arithmetic logic unit).clm.	0	<u>L2</u>
<u>L1</u>	(processing element and vector register and block register and scalar register).clm.	2	<u>L1</u>

END OF SEARCH HISTORY